IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Bruce Whitefield et. al.

Serial No.:

10/628,986

Filed:

July 28, 2003

For:

Method of Mapping Logic Failures

in an Integrated Circuit Die

99999999

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AUG 1 2 2004

Atty Docket:

Examiner:

Group Art Unit:

/ 03-0593

2643

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Manu Kashyap

INFORMATION DISCLOSURE STATEMENT

\$ \$ \$

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 12 Auc 04

Respectfully submitted,

Timothy Croll

Reg. No. 36,771

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LSI LOGIC CORPORATION
Office of General Counsel
1621 Barber Lane
M/S D-106
Milpitas, CA 95035

Fax: (408) 433-7460

Date:

August 12, 2004

To

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Manu Kashyap, Intellectual Property Paralegal

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10/628,986

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		Comp	lete if Known	
	_	Application Number	10/628,986	_
FEE TRANSMIT	TAL	Filing Date	July-28, 2003	
Potent Fees are subject to annual revision on	October 1.	First Named Inventor	Bruce Whitefield	
These are the fees effective Octobers, i	eniity statement,	Group Art Unit	2643	
herwise large entity fees must be paid. See Forms See 37 C.F.R. 1.27 and 1.28.	PTO/SB/09-12	Examiner Name		
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METHOD OF PAYMENT (check one)	FEE CALGULATION (continued)					
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Deposit 12-2252	105	130	205	65	Surcharge - late filing fee or oath	
Number	127	50	227	25	Surcharge - late provisional filing fee or cover sheet	
Account LSI LOGIC CORPORATION	139	130	139	130	Non-English specification	
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103 22 203 11 Claims in excess of 20					final rejection (37 CFR 1,129	
102 62 202 41 Independent claims in excess of 3	149	790	249	395	For each additional invention to be examined (37 CFR 1.129 (b))	1 1
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Typed or printed name Timothy Croll					Reg. Number 36,771	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date	July-28, 2003	
			LICANI	First Named Inventor	Bruce Whitefield	
			•	Group Art Unit	2643	
				Examiner Name		
Sheet	1 .	of	1	Attorney Docket No.	/ 03-0593	

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No.	U.S. Patent Document Number Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
. :	-	5,392,222	Noble; Alan C. (Sunnyvale, C	02-21-1995		
		5,568,563	Tanaka et al.	10-22-1996		
		5,572,636	Sakuraba et al.	11-05-1996		
		5,731,986	Shy-Lun Yang	03-24-1995		
	•	5,751,581	Tau et al.	05-12-1998		

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